

SIGMA DELTA TRANSDUCER INTERFACE: FEATURES

FEATURES THAT DIFFERENTIATE THE AD77XX FAMILY													
	#	#						+3V	100 nA		READ	Software	Hardware
	#	Diff	Single	20 uA	200 uA	PGA	Input	Operation	I-source	Internal	WRITE	Power	Power
	BITS	Inputs	Ended	Current	Current	1-128	Atten.		Open	+2.5V	COEFF	Down	Down
			Inputs	Source	Source				T/Couple	Reference			
AD7701 & AD7703: BRICK WALL FILTER RESPONSE, CLOCK PROGRAMMABLE													
AD7701	16		1							X			X
AD7703	16		1							X			X
AD280	15		4		X	X	X		X	X			
AD77xx: \sin^3 FILTER RESPONSE, SOFTWARE PROGRAMMABLE													
AD7705													
AD7730	24	2		6 bit "TARE" DAC		1>8						X	
AD7731	24	2	5										
AD7710	24	2		X		X			X	X	X	X	
AD7711	24	1	1		2	X				X	X	X	
AD7712	24	1				X	X		X	X	X	X	X
AD7713	24	2			2	X	X		X		X	X	X
AD7714	24	3 or >	5			X		X			X		X
AD7715	16	1				X		X					
FOUR CHANNEL													
AD7716	22		4										